

**ABSTRACT OF THE DISCLOSURE****CONCURRENT MODIFICATION AND EXECUTION OF INSTRUCTIONS**

- 5           A method, program, and system for modifying computer  
program instructions during execution of those  
instructions are provided. The invention comprises  
writing a first instruction into a memory location,  
wherein the instruction is a patch class instruction.
- 10   This first instruction is then fetched from the memory  
location and executed. Concurrent with execution of the  
first instruction, the memory location is overwritten  
with a second instruction, which is also a patch class  
instruction. Because the first and second instructions
- 15   are patch class instructions, if a program is executing  
from the memory location, or returns to execute from that  
location, it will fetch and execute either the first  
instruction or the second instruction. In one  
embodiment, reconciling the processor's execution
- 20   pipeline with the memory location will ensure that the  
second instruction is fetched and executed if the program  
returns to execute from that location.

FOIA b 7 - D 310